SMD Power Inductor

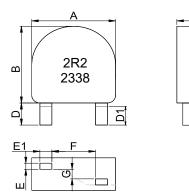
1. Features

- 1. Low loss realized with low DCR.
- 2. High performance realized by metal dust core.
- 3. Ultra low buzz noise, due to composite construction.
- 4. 100% Lead(Pb)-Free and RoHS compliant.
- 5. High reliability -Reliability test complied to AEC-Q200.

2. Applications

Automotive applications.

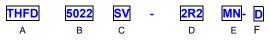
3. Dimensions



Α	В	С	D	D1	E	E1	F	G
51±0.3	51±0.3	21.6±0.4	$12.8\!\pm\!0.8$	11.5 ± 0.8	4.0 ± 0.25	7.0 ± 0.25	$25.0\!\pm\!0.5$	7.1±0.4

Unit:mm

4. Part Numbering



BxC

2R2=2.2uH

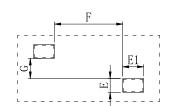
A: Series

- **B**: Dimension C: Type
- D: Inductance
- E: Inductance Tolerance
- F: DateCode
- M=±20% N:No Coating Marking 2R2 and 2338 (23 YY, 38WW,follow production date).

Halogen-free

THFD5022SV-2R2MN-D

Recommend PC Board Pattern



Plug-in-type						
E(mm) E1(mm) F(mm) G(mm)						
4.5	7.5	24.5	6.6			





△ △: DCR Test

5. Specification

Part Number	Inductance I rms (uH) ±20%		А) Тур	I sat (A)		DCR (mΩ)	
Fait Number	@0A	40℃ rise	100℃ rise	1	2	Тур	Мах
THFD5022SV-2R2MN-D	2.2	125	187	190	280	0.21	0.23

Note:

1. Test frequency : Ls : 500KHz /1.0V.

2. All test data referenced to 25°C ambient.

4. Heat Rated Current (Irms) will cause the coil temperature rise approximately ΔT of 40 °C & approximately ΔT of 100 °C

5. Saturation Current (Isat 1) will cause L0 to drop approximately 20%.

Saturation Current (Isat 2) will cause L0 to drop approximately 30%

6. The part temperature (ambient + temp rise) should not exceed 155 °C under worst case operating conditions. Circuit design, component, PCB trace size and thickness,airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

7. Irms Testing : Temperature rise is highly dependent on many factors including pcb land pattern, trace size, and proximity to other components. Therefore temperature rise should be verified in application conditions.

8.Rated DC current: The lower value of Irms and Isat.

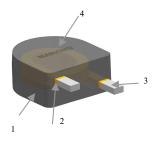






^{3.} Testing Instrument(or equ) : Agilent 4284A, E4991A, 4339B, KEYSIGHT E4980A/AL, chroma3302, 3250, 16502.

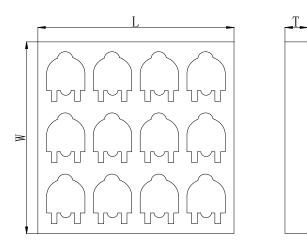
6. Material List



NO	Items	Materials
1	Core	Alloy Powder
2	Wire	Polyester Wire or equivalent.
3	Solder	100% Pb free solder
4	Ink	Halogen-free ketone

7. Packaging Information

(1) Tray Dimension



Series	Size	w	L	т			
THFD	5022	285 ref	265 ref	30 ref			
Unit:mm	Unit:mm						

(2) Packaging Quantity

THFD	5022	piece weight
Tray	12 PCS	286g ref

8. Reliability and Test Condition

Item	Performance	Test Condition			
Operating temperature	-55~+155°C (Including self - temperature rise)				
Storage temperature and 110~+40°C,50~60%RH (Product with taping) Humidity range 255~+155°C (on board)					
Electrical Performance Test					
nductance		HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.			
DCR	Refer to standard electrical characteristics list.	CH16502,Agilent33420A Micro-Ohm Meter.			
Saturation Current (Isat)	Approximately △L20%(Isat 1) Approximately △L30%(Isat 2)	Saturation DC Current (Isat) will cause L0 to drop			
leat Rated Current (Irms)	Approximately △T40°C &△T100°C	Heat Rated Current (Irms) will cause the coil temperature rise $T(^{\circ}_{C})$. 1.Applied the allowed DC current			
Reliability Test		2. Temperature measured by digital surface thermometer			
High Temperature Exposure(Storage) AEC-Q200 Temperature Cycling AEC-Q200		Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles Temperature: 155±2°C (Inductor) Duration : 1000hrs Min. Measured at room temperature after placing for 24±2 hrs Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles Condition for 1 cycle Step1: -55±2°C 30min Min.(Inductor) Step2: 155±2°C transition time 1min MAX. Step3: 155±2°C 30min Min. Step4: Low temp. transition time 1min MAX. Number of cycles: 1000			
Moisture Resistance (AEC-Q200)	Appearance : No damage. Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	Measured at room temperature after placing for 24±2 hrs t=24 hours/cycle. Note: Steps 7a & 7b Unpowered.			
Biased Humidity (AEC-Q200)					
High Temperature Operational Life (AEC-Q200)		Preconditioning: Run through IR reflow for 3 times. (IPC/JEDECJ-STD-020E Classification Reflow Profiles Temperature: 155±2℃(Inductor) Duration : 1000hrs Min. With 100% rated current. Measured at room temperature after placing for24±2hrs			
External Visual	Appearance:No damage.	Inspect device construction, marking and workmanship. Electrical Test not required.			
Physical Dimension According to the product specification size measurement		According to the product specification size measurement			
Resistance to Solvents	Appearance:No damage.	Add aqueous wash chemical - OKEM clean or equivalent.			
Appearance : No damage. Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value		Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles) Test condition Type Peak value Normal Vave Velocity (g's) duration (D) (ms) rorm change (Vi)ft/sec SMD 100 6 Half-sine 12.3 Lead 100 6 Half-sine 12.3 3 shocks in each direction along 3 perpendicular axes(18 shocks).			

	Performance	Test Condition			
Vibration		Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles) Oscillation Frequency: 10Hz~2KHz~10Hz for 20 minute Equipment : Vibration checker Total Amplitude: 5g Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations) *			
		Test condition::(MIL-STD-202 Condition B)			
		, , ,			
		Temperature (°C) Time (s) Temperature ramp/immersion and emersion rate Number of heat cycles 260 ±5 10 ±10 5 10 ±10			
		(solder temp) 10 ±1 25mm/s ±6 mm/s 1			
		Depth: completely cover the termination			
		Continental			
Resistance to Soldering Heat Ir C R	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the	Temperature time 25°C to peak temperature component T _{Sonn} T _{sonn}			
s	specification value				
		Component Increase Values - 20m add 1 forces values - 20m add 1 forces va			
Thermal shock (AEC-Q200)		Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Condition for 1 cycle Step1: -55±2℃ 15±1min(Inductor) Step2: 155±2℃ 15±1min Number of cycles : 300 Measured at room fempraturc after placing fo24±2hrs			
ESD A	Appearance : No damage.	Direct Contact and Air Discharge PASSIVE COMPONENT HBM ESD Discharge Waveform to a Coaxial Target Test method: AEC-0200-002 Test mode : Contact Discharge			
		Discharge level : 4 KV (Level: 2) a. Method B1, 4 hrs @155°C dry heat @255°C±5°C Test time:5 +0/-0.5 seconds.			
	More than 95% of the terminal electrode should be covered with solder \circ	b. Method D category 3. (steam aging 8hours ± 15 min)@ 260°C±5°C			
Solderability s					

Board Flex	Appearance : No damage	Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles Place the 100mm X 40mm board into a fixture similar to the one shown in below Figure with the component facing down. The apparatus shall consist of mechanical means to apply a force which will bend the board (D) x = 2 mm minimum. The duration of the applied forces shall be 60 (+ 5) sec. The force is to be applied only once to the board. Support Solder Chip Printed circuit board before testing 45±2 45±2 45±2 Result-#
Terminal Strength(SMD)	Appearance : No damage	Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles With the component mounted on a PCB with the device to be tested, apply a 17.7 N (1.8 Kg) force to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested.

Note : When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition.

9. Soldering Specifications

(1) Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

(2) Soldering Reflow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020E)

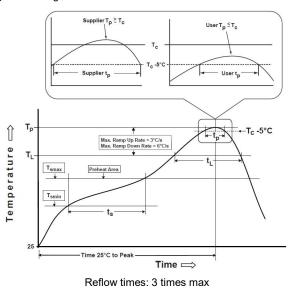
(3) Iron Reflow:

- Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.(Fig. 2)
- Preheat circuit and products to 150 $^\circ\!{\rm C}$ • 355°C tip temperature (max)

Table (1.1): Reflow Profiles

• 1.0mm tip diameter (max)

- · Never contact the ceramic with the iron tip Use a 20 watt soldering iron with tip diameter of 1.0mm Limit soldering time to 4~5sec.
- Fig.1 Soldering Reflow



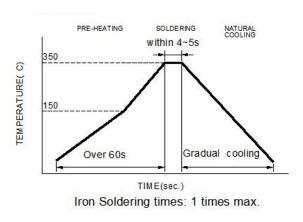


Fig.2 Iron soldering temperature profiles

Soldering iron Method : 350± 5℃ max

Profile Type:	Pb-Free Assembly
Preheat	
-Temperature Min(T _{smin})	150 ℃
-Temperature Max(T _{smax})	200 ℃
-Time(t_s)from(T_{smin} to T_{smax})	60-120seconds
Ramp-up rate(T∟to T _p)	3℃/second max.
Liquidus temperature(T _L)	217℃
Time(t_L)maintained above T_L	60-150 seconds
Classification temperature(T _c)	See Table (1.2)
$Time(t_p)$ at Tc- $5^\circ\!\mathbb{C}$ (Tp should be equal to or less than Tc.)	*< 30 seconds
Ramp-down rate(T_p to T_L)	6℃ /second max.
Time 25 $^\circ\!\!\!\!^{^\circ}\!\!\!^{^\circ}$ to peak temperature	8 minutes max.

Tp: maximum peak package body temperature, Tc: the classification temperature.

For user (customer) Tp should be equal to or less than Tc.

* Tolerance for peak profile temperature (Tp) is defined as a supplier minimum and a user maximum.

Table (1.2) Package Thickness/Volume and Classification Temperature (T	c)	
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	Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
	<1.6mm	260°C	260°C	260°C
PB-Free Assembly	1.6-2.5mm	260°C	250°C	245℃
	≥2.5mm	250°C	245°C	245°C

Reflow is referred to standard IPC/JEDEC J-STD-020E °

10. Notes

- (1) When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition
- (2) This power choke coil itself does not have any protective function in abnormal condition such as overload, short-circuit and open-circuit conditions, etc. Therefore, it shall be confirmed as the end product that there is no risk of smoking, fire, dielectric withstand voltage, insulation resistance, etc. in abnormal conditions to provide protective devices and/or protection circuit in the end product.
- (3) When this power choke coil was used in a similar or new product to the original one, sometimes it might not be able to satisfy the specifications due to different condition of use.
- (4) Dielectric withstanding test with higher voltage than specific value will damage insulating material and shorten its life.
- (5) This power choke coil must not be used in wet condition by water, coffee or any liquid because insulation strength becomes very low in this condition.
- (6) Please consult our company to confirm the reliability of the process required to wash or use or exposure to a chemical solvent used in this product. PCB washing tested to MIL-STD-202 Method - and dry it off immediately -
- (7) The rated current as listed is either the saturation current or the heating current depending on which value is lower.
- (8) If this power choke is dipped in the cleaning agent, such as toluene, xylene, ketone, and ether system, there is a possibility that the performance decreases greatly, and marking disappearnc.
- (9) The high power ultrasonic washing may damage the choke body.
- (10) Before use, the user should determine whether this product is suitable for their own design, Our company only guarantees that the product meets the requirements of this specification.

Application Notice

- Storage Conditions
- To maintain the solderability of terminal electrodes:
- 1. TAI-TECHproducts meet IPC/JEDEC J-STD-020E standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40°C and 60% RH.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

11. Typical Performance Curves

